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**IN THE UNITED STATES PATENT AND TRADEMARK OFFICE**

Applicant : Ryuta NAKANISHI et al.

Serial No : Not Yet Assigned (National Stage of PCT/JP2004/019102)

I.A. Filed : December 21, 2004

For : CACHE MEMORY AND ITS CONTROLLING METHOD

**PRELIMINARY AMENDMENT**

Commissioner of Patents  
U.S. Patent and Trademark Office  
Customer Service Window, Mail Stop \_\_\_\_\_  
Randolph Building  
401 Dulany Street  
Alexandria, VA 22314

Sir:

Prior to the examination of the above-identified patent application on the merits, the Examiner is respectfully requested to amend the specification and claims as follows:

**Amendments to the Specification** begin on page 2 of this paper.

**Amendments to the Claims** are reflected in the listing of claims which begins on page 3 of this paper.